

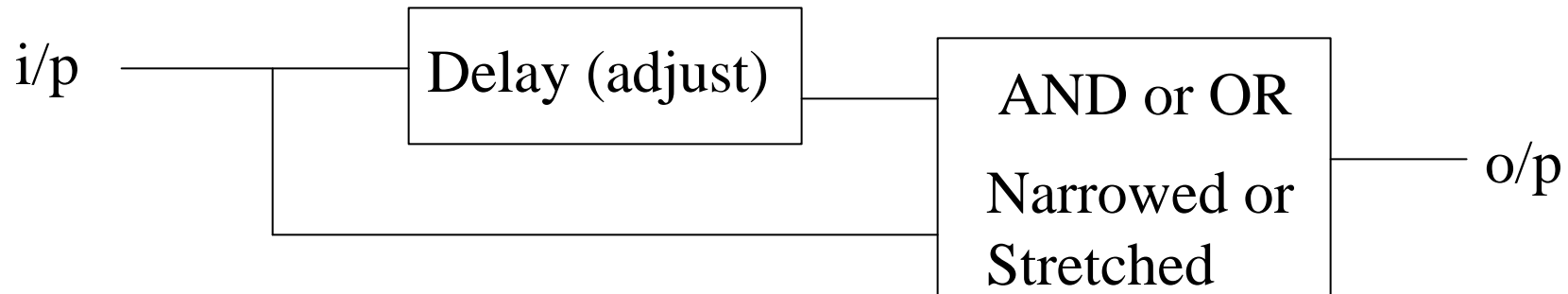
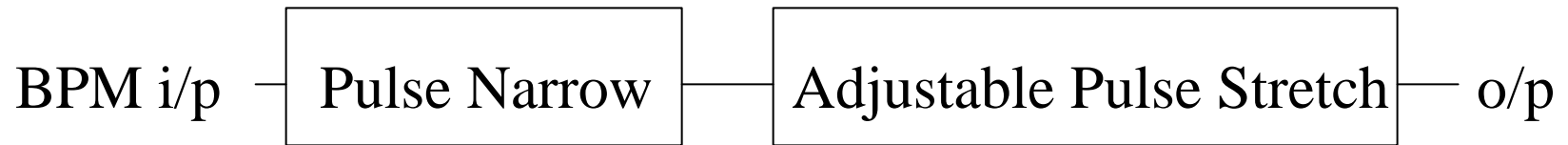
ASIC Status

- **VDC** radiation tests completed successfully.
- **DORIC4A** radiation tests still to be done.
- **BPM-4B** (fix to allow o/p current to be adjusted) due back mid December.
- **DRX-12** due back mid December. Should be final prototype.
- **BPM-12** submit 2 December? Many changes compared to BPM-4...

BPM-12 Changes

- **New o/p driver** for common cathode VCSELs. Mitel will not produce common anode VCSELs. CMOS driver, design by Dave White (==> Figure).
 - **Simplified BiPhase Mark** encoding.
 - **Improved mark:space ratio** adjustments for optical signal (==> Figure).
 - AMS o/p pads.
 - **12 channels.**
- (ECL receiver block, coarse and fine delay unchanged).**

Mark: Space Ratio Adjustment



Some Simulation Results

- Mark:Space Ratio adjustment (==> Figure).
- Fine delay adjust (==> Figure).
- Coarse Delay adjustment (==> Figure).