2\textsuperscript{nd} Stage Board: Functional layout

- Main Board (pSSB)
  - External Memory
  - Track Fitter FPGA
    - Real 12-Layer Tracks
  - Extrapolator FPGA
    - 8 Layer Tracks + Hit Lists + Layer Map + SectorID
    - 8-Lyr Tr + SS
    - 8-Lyr Tr
  - External Memory
  - Hit Warrior FPGA
    - Fanout

- RTM
  - DF Inputs (2)
  - AUX Inputs (4)
  - Intracrate SSB track flow (1)
  - Intracrate SSB overlap only (1)
  - Intercrate SSB overlap only (1)
  - + Flow CTL on each