Block Diagram of FE ASIC Test Stand

NOTE:
1. DIFF x 16 means 16 differential pairs.
2. ADC driver circuit is consist of ADA4899 and ADA4932.
3. Two calibration pulse generators are implemented. One is based on the LAL low-offset amplifier ASIC, and other one is based on the commercial low-offset amplifier OPA735.

FE Test Board

- EXT +11V
- EXT +7V
- EXT -7V
- EXT +4V
- EXT -4V

For more details on the components and connections, please refer to the block diagram.