The diagram illustrates the process flow for the FELIX/DAQ system with a focus on the front-end and data processing components. The front-end sends data to the input stage, which is followed by configurable remapping. The remapped data then goes through the pulse processing stage, which includes energy summing. The energy summing results are then sent to the data reduction stage. The TTC L0A/L1A and fragment builder components are integrated into the process flow, ensuring precise data buffer handling, gain selection, and energy summing. The FEXes and Global Event components are connected to the data flow, indicating the system's integration with external data processing units.