MDT Hits
Max 400 kHz / tube

3x GBT rx
(18 mezz)

10 μs = 3600 words
35 μs = 12600 words
(FPGA block RAM)

Per pipeline
432 tubes (24*18)
86 MHz avg rate (200kHz tube rate)
360 MHz pipeline clock

1 MHz L0 Accepts

Total 18 pipelines per sector (6 each inner/middle/outer)

FIFO 512-1k words

MUX

Multiplex / readout logic would read hits and deliver to FELIX based on load sharing between links.

FELIX Buffer
(External SDRAM)

Readout Load Balancing

~ 12 FELIX links @ 9.6Gbps
(7.6 Gbps usable)